

#8/B 2815  
8-1602  
Mwesi



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:  
MANFRED HORSTMANN  
ROLF STEPHAN  
KARSTEN WIECZOREK  
STEPHEN KRUEGEL

Examiner: J. Fenty  
Group Art Unit: 2815  
Att'y Docket: 2000.064300

RECEIVED  
AUG - 8 2002  
TECHNOLOGY CENTER 2800

Serial No.: 09/847,622

Filed: May, 2, 2001

For: FIELD EFFECT TRANSISTOR WITH  
REDUCED GATE DELAY AND  
METHOD OF FABRICATING THE  
SAME

**RESPONSE TO OFFICE ACTION DATED MAY 8, 2002**

<p>CERTIFICATE OF MAILING 37 C.F.R 1.8</p> <p>I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on the date below:</p> <p>July 25, 2002 Date</p> <p><i>Sharon W. Dart</i> Signature</p>	
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Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

This paper is submitted in response to the Office Action dated May 8, 2002, for which the three-month date for response is August 8, 2002.

It is believed that no fee is required in connection with this response. However, the Assistant Commissioner is authorized to deduct any fees required under 37 C.F.R. §§ 1.16 to 1.21 from the Advanced Micro Devices, Inc. Deposit Account No. 01-0365/2000.064500/DE0066. In the event the monies in that account are insufficient, the